

Paper Review Guidelines for TPSC

STEP (3) : PAPER REVIEW

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Analog, Mixed-Signal & RF Design

NO	Paper Code (Affiliation)	Paper Title	Reviewers (Each paper receives at least 3 reviews)	Add Reviewer
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2	TW011 (Kyushu Institute of Technology, National Taiwan University of Science)	On-Chip Test Clock Validation Using A Time-to-Digital Converter in FPGAs <input type="button" value="download"/>		<input type="button" value="Select"/>

➡ You can download the report for your reference.

Analog

NO	Paper Code (Affiliation)	Paper Title	Reviewers	Add reviewer
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2	TW011 (ITRI)	A High-Performance Current-Mode DC-DC Buck <input type="button" value="download"/>		<input type="button" value="Select"/>
3	TW013 (Chainyi)	TESTTEST <input type="button" value="download"/>		<input type="button" value="Select"/>